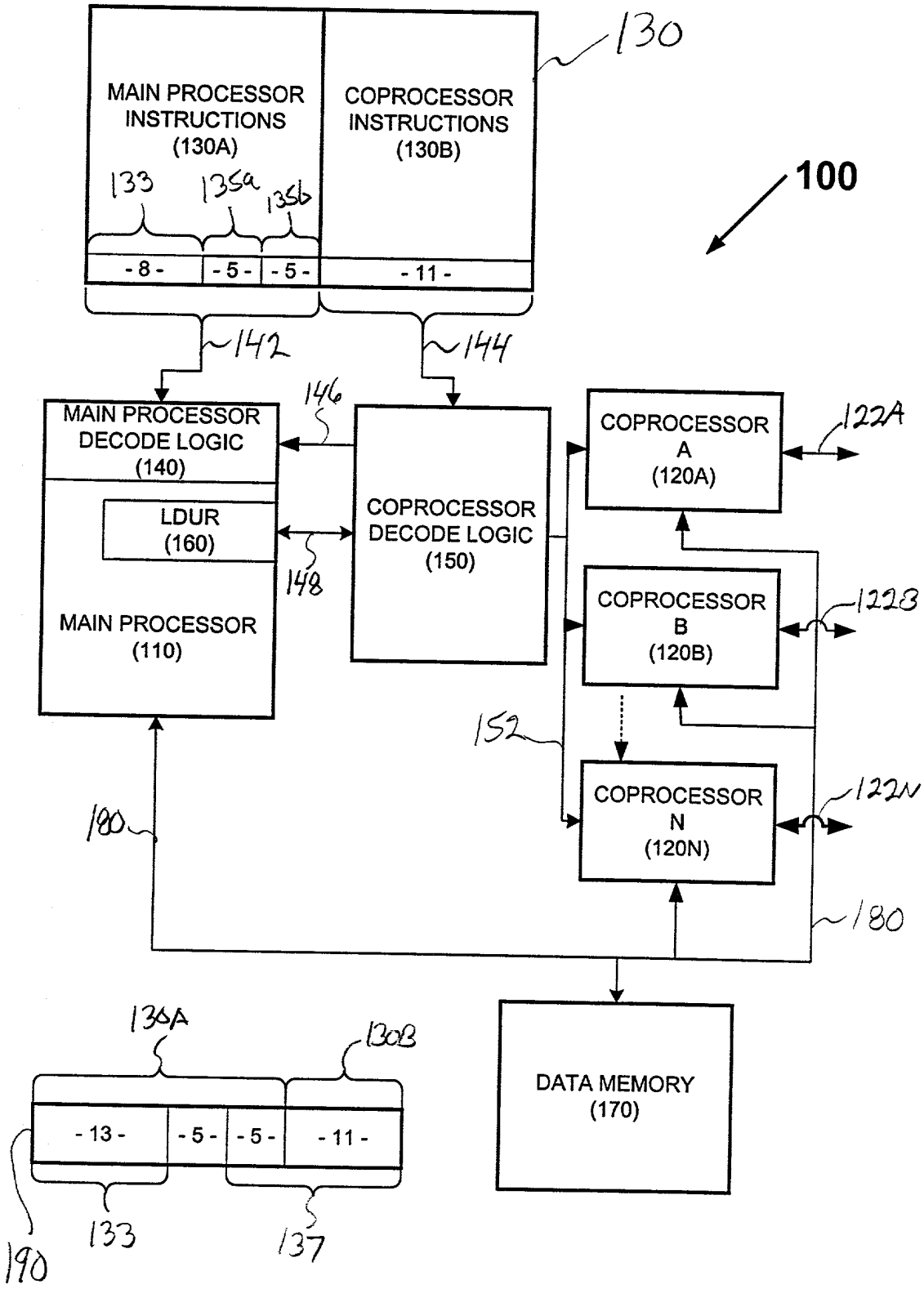
130
MAIN PROCESSOR INSTRUCTIONS (130A)
COPROCESSOR INSTRUCTIONS (130B)
133
135a
135b
- 8 -
- 5 -
- 5 -
- 11 -
100
142
144
146
MAIN PROCESSOR DECODE LOGIC (140)
LDUR (160)
MAIN PROCESSOR (110)
148
COPROCESSOR DECODE LOGIC (150)
COPROCESSOR A (120A)
122A
COPROCESSOR B (120B)
122B
152
COPROCESSOR N (120N)
122N
180
180
DATA MEMORY (170)
135A
130B
- 13 -
- 5 -
- 5 -
- 11 -
190
133
137

FIGURE 1

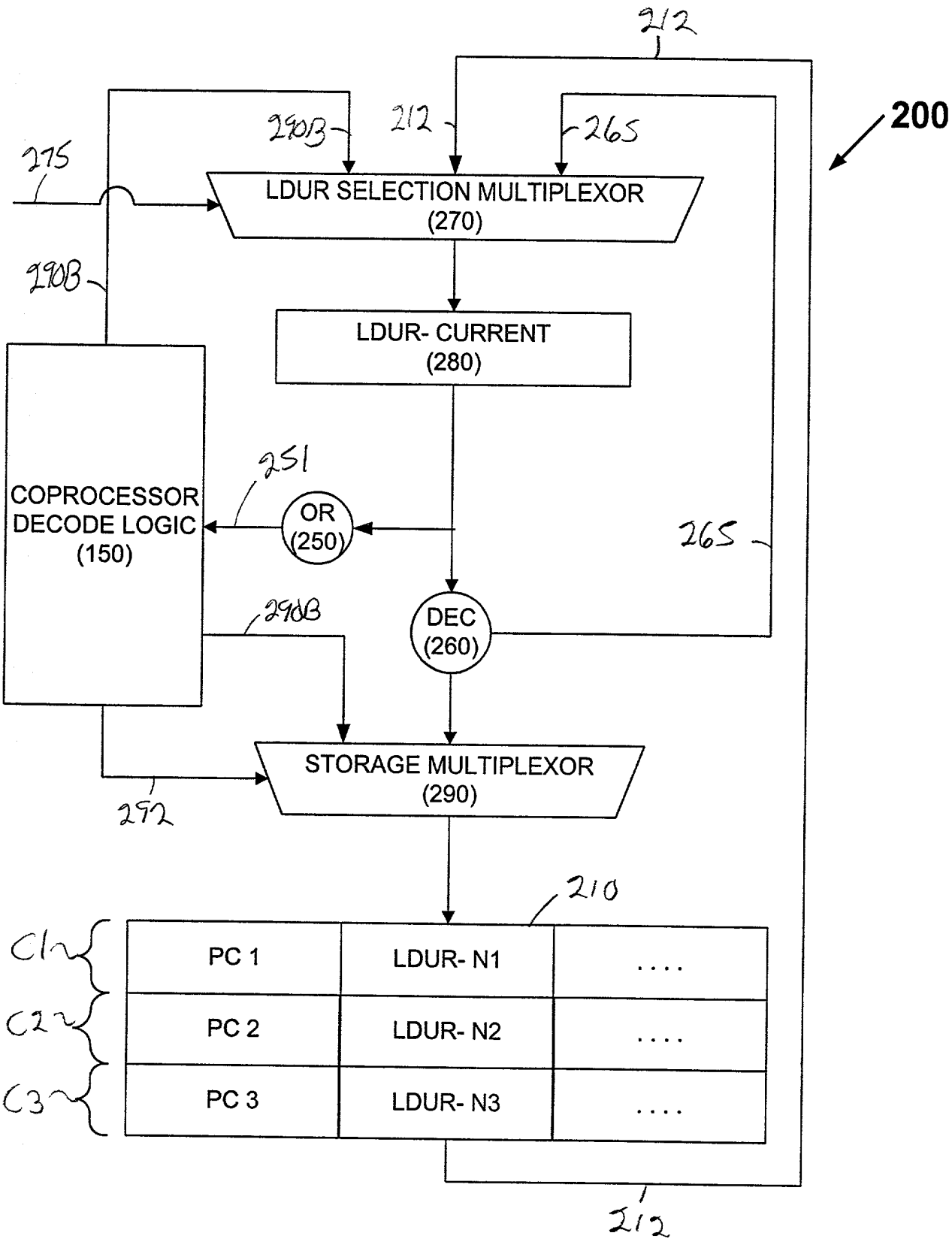
212
200
290B
212
265
275
LDUR SELECTION MULTIPLEXOR
(270)
290B
LDUR- CURRENT
(280)
COPROCESSOR
DECODE LOGIC
(150)
251
OR
(250)
265
290B
DEC
(260)
STORAGE MULTIPLEXOR
(290)
292
210
C1
C2
C3
PC 1
LDUR- N1
....
PC 2
LDUR- N2
....
PC 3
LDUR- N3
....
212

FIGURE 2